

PROPRIETARY NOTICE:
THE INFORMATION CONTAINED ON THE DOCUMENT IS CONSIDERED TO BE CONFIDENTIAL, MATERIAL, PROPRIETARY TO DUST NETWORKS AND IS SOLELY FOR THE USE OF THE PERSONNEL OF DUST NETWORKS. NO PART OF THIS INFORMATION PAPER(S) SHALL BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT THE WRITTEN CONSENT OF A DUST NETWORKS OFFICER.

REVISION RECORD		
REV.	DESCRIPTION:	DATE:
REV. 1	ORIGINAL CONCEPT	5/12/06
REV. 1	Initial Release per ESD 386	8/28/07
REV. 2	Initial Release	9/15/07
REV. 2	Change nothing options per ESD 386	10/12/07
REV. 2	Change nothing options per ESD 386	11/12/07
REV. 2	Change nothing options per ESD 468	11/28/07
REV. 7	Change nothing options per ESD 412	12/07/07
REV. 8	Initial Release to reflect new-HW5 assembly per ESD412	12/07/07

NOTES (UNLESS OTHERWISE SPECIFIED):

1. ASSEMBLE AND INSPECT IN ACCORDANCE WITH IPC STANDARD IPC-610A-C.
2. ASSEMBLE WITH NON-ROHS PROCESS
3. WATCH FOR POLARITY OR ORIENTATION MARKINGS ON THE FOLLOWING COMPONENTS: U1, U2
4. THE FOLLOWING COMPONENTS CAN BE POPULATED IN EITHER ORIENTATION:
ALL RESISTORS, INDUCTORS AND NON-POLARIZED CAPACITORS.
5. INSTALL J1 AND J2 ON BACKSIDE OF THE BOARD. DO NOT INSTALL PIN 3 ON J1 AND J2.
6. SQUARE PAD DENOTES PIN ONE OF CONNECTORS AND HEADERS.
7. INSTALL RF SHIELD, 440-0041 ON TOPSIDE. ORIENT RF TRIANGLE AS SHOWN ON DRAWING. SOLDER THE PINS FROM THE BACKSIDE OF THE BOARD USING NO-CLEAN FLUX.
8. COMPLETED ASSEMBLY TO BE PACKAGED IN ANTI-STATIC PACKAGING THAT WILL PROTECT FROM DAMAGE AND DETERIORATION.

